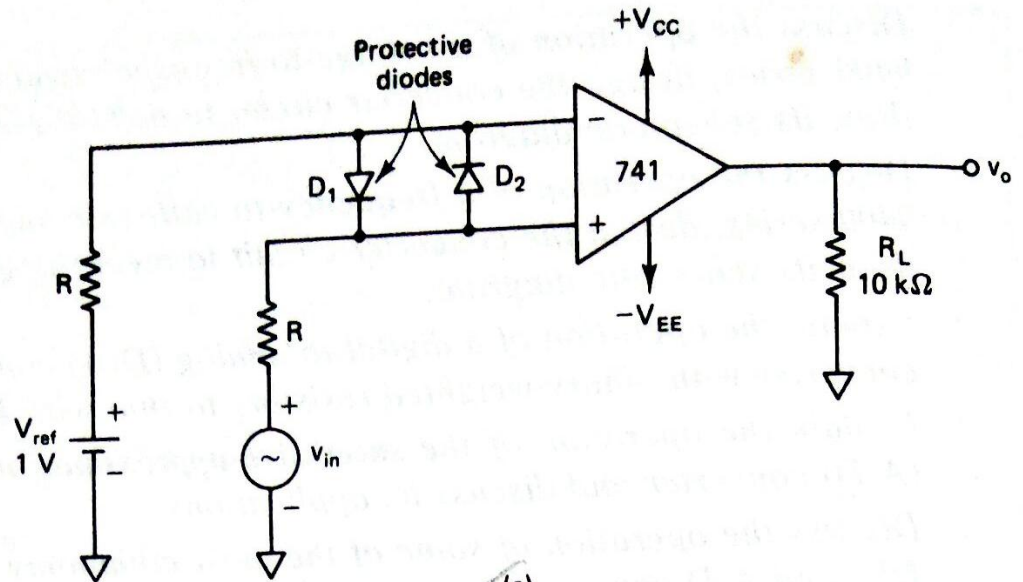


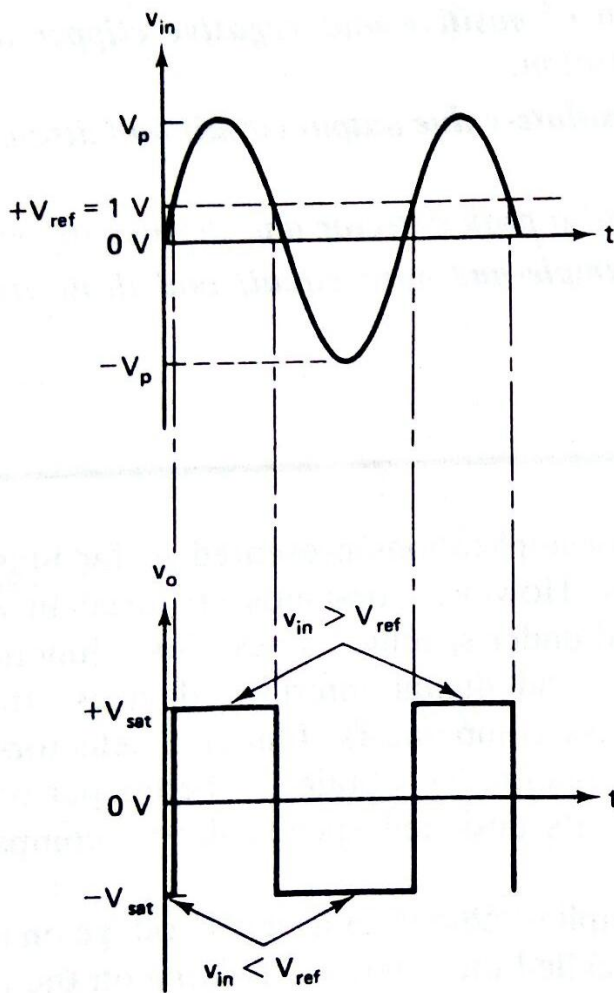
# Comparator

- A comparator, as its name implies, compares a signal voltage on one input of an op-amp with a known voltage called the **reference voltage** on the other input.
- Basic circuit of a comparator using op-amp is shown in Fig. where a reference voltage  $V_{ref}$  equal to 1V is applied at inverting input and a sinusoidal voltage  $V_{in}$  is applied at noninverting input.
- As it is shown in Fig. whenever  $V_{in} > V_{ref}$  output goes to positive saturation voltage  $+V_{sat}$  because **op-amp is working in open loop**. Similarly when  $V_{in} < V_{ref}$  output goes to negative saturation voltage  $-V_{sat}$ . Thus  $V_o$  changes from one saturation level to another whenever  $V_{in} = V_{ref}$ .
- As it is evident from Fig. that an analog input voltage (sinusoidal voltage) is getting converted into a digital voltage (square wave) in the process of comparison, that's why comparator is termed a type of **analog-to-digital convertor**.
- At any given time the  $V_o$  waveform shows whether  $V_{in}$  is greater or less than  $V_{ref}$ . Therefore comparator is sometimes also called **voltage-level detector**.
- In Fig. whenever  $V_{in}$  becomes too high from  $V_{ref}$  then this high difference voltage may exceed the rating of op-amp and cause damage to op-amp. Therefore to protect the op-amp diode D2 is used because when difference voltage exceeds, D2 will clamp the difference voltage to 0.7 V.
- Similarly when  $V_{in}$  is too low from  $V_{ref}$ , diode D1 will come into picture.

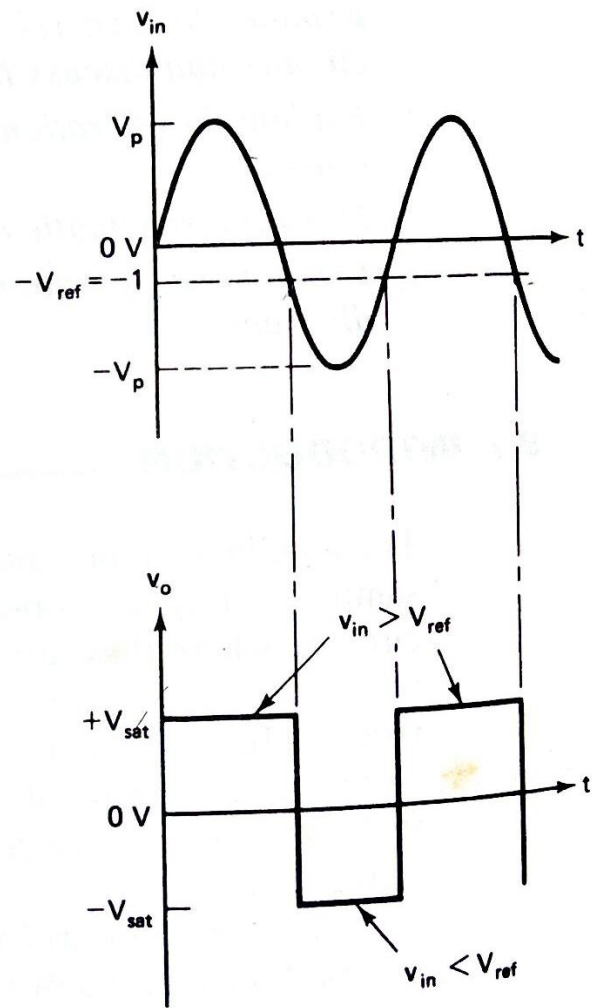
- That's why D1 & D2 are known as *clamp diodes*.
- In Fig.(c) reference voltage has been taken as -1 V.
- Since in this circuit the voltage to be compared is applied at noninverting input that's why this circuit is termed as *noninverting comparator*.



(a)



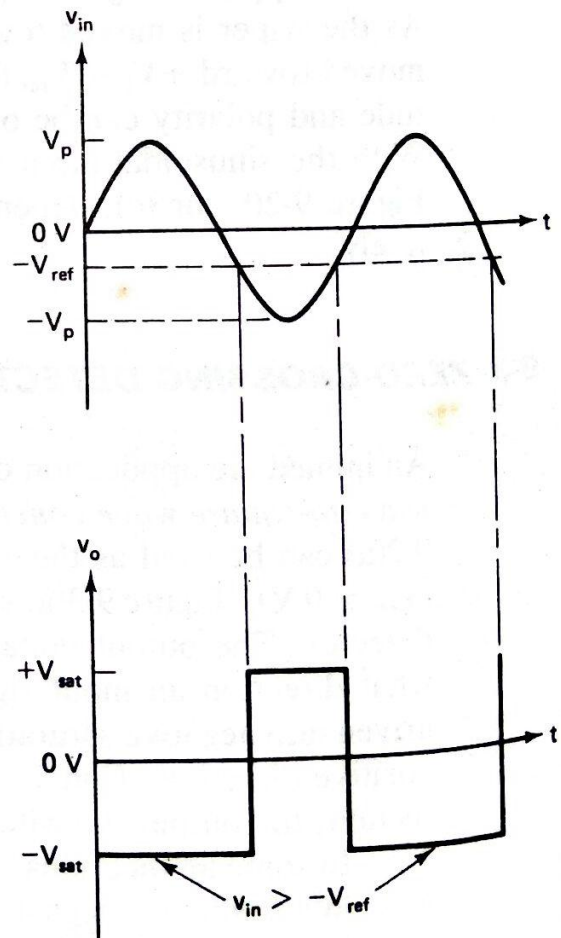
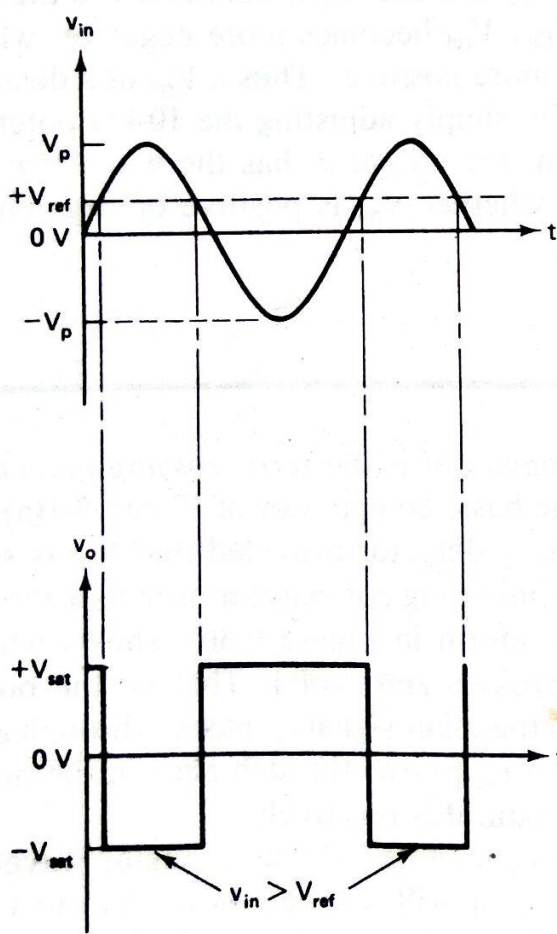
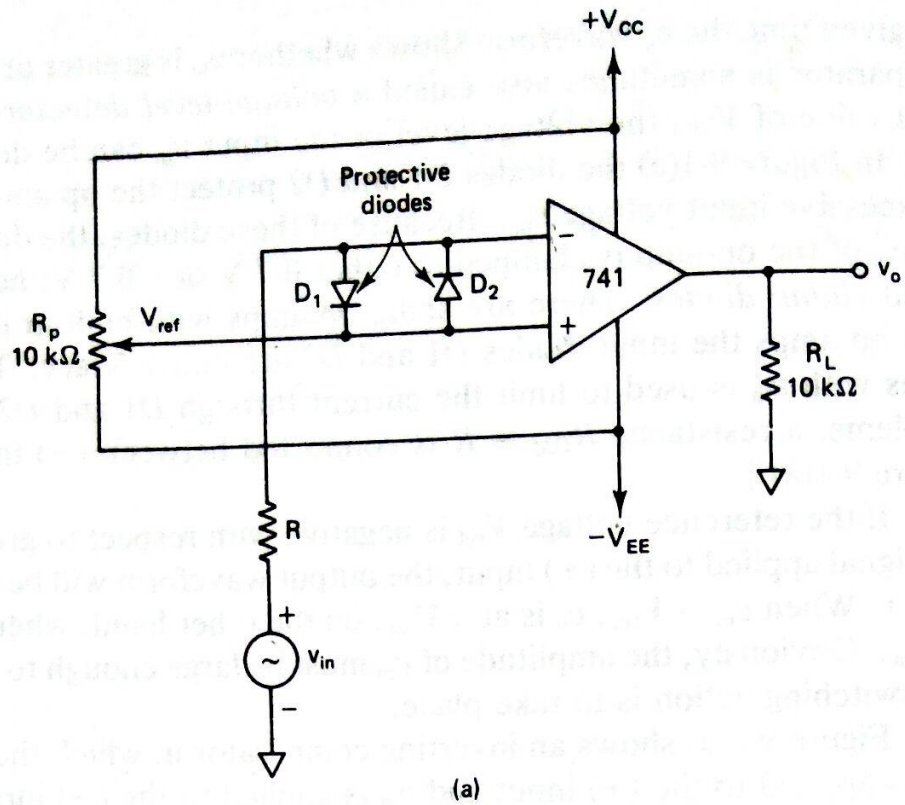
(b)



(c)

**Fig: Noninverting comparator with input and output waveforms**

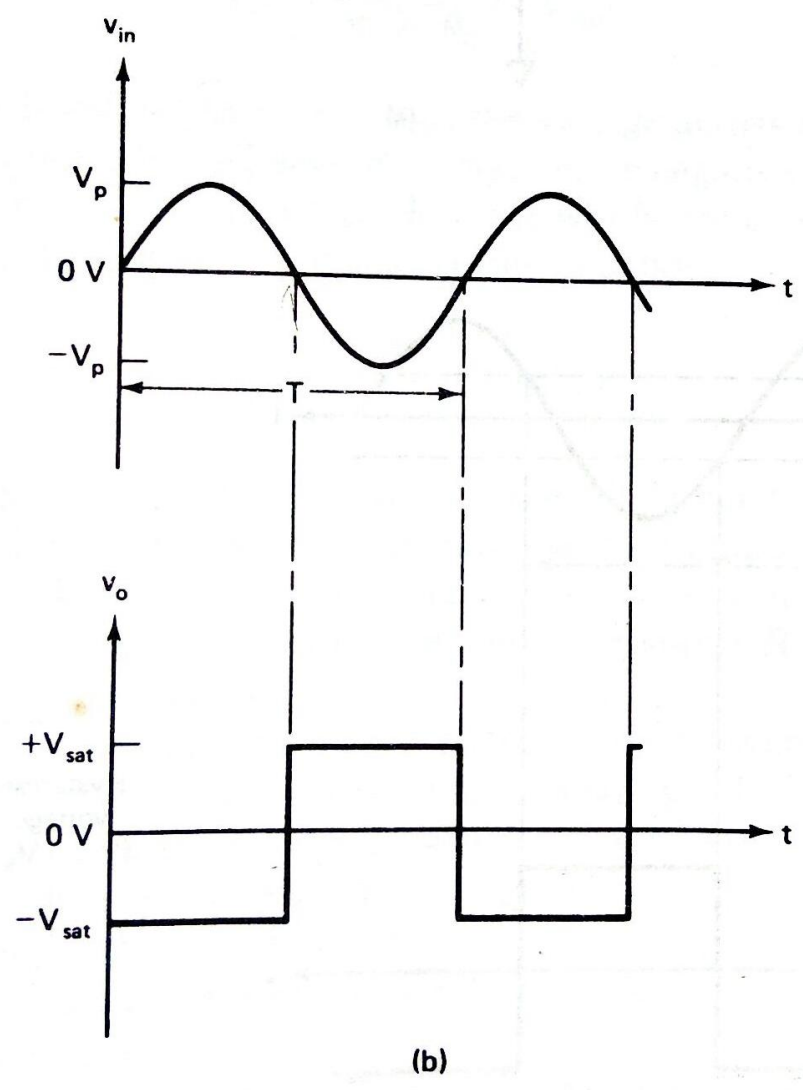
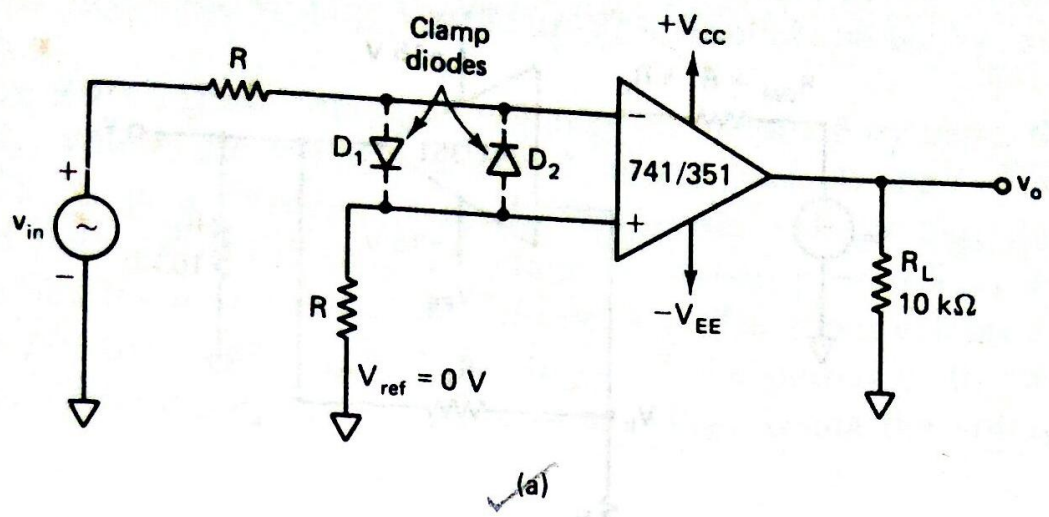
- Following circuit shows an inverting comparator.
- In this circuit,  $V_{ref}$  is obtained by using a  $10\text{-k}\Omega$  potentiometer that forms a voltage divider with the dc supply voltages  $+V_{cc}$  and  $-V_{EE}$  and the wiper connected to the (+) input.
- The rest of the working is similar to noninverting comparator.



**Fig. Inverting comparator with input and output waveforms**

## Zero-Crossing Detector

- If we set  $V_{ref} = 0\text{ V}$  then the input voltage will be compared to zero volt and the corresponding circuit and waveforms will be as follows.
- This circuit is known as *zero crossing detector* or *sine-wave-to-square-wave convertor*.



**Fig: Zero crossing detector with its input and output waveforms**

